

#### INVENTOR INFORMATION

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#### CORRESPONDENCE INFORMATION

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#### APPLICATION INFORMATION

Title Line One:: Semiconductor Device Including A Surge P  
Title Line Two:: rectifying Circuit  
Total Drawing Sheets:: 29  
Formal Drawings?:: No  
Application Type:: Utility  
Docket Number:: 4041P-20/DVA  
Secrecy Order in Parent Appl.?:: No

#### CONTINUITY INFORMATION

This application is a:: DIVISION OF  
> Application One:: 09/526,971  
Filing Date:: 03-16-2000

#### PRIOR FOREIGN APPLICATIONS

Foreign Application One:: JPSN 11-210908  
Filing Date:: 07-26-1999  
Country:: Japan  
Priority Claimed:: Yes  
Foreign Application Two:: JPSN 11-75423  
Filing Date:: 03-19-1999  
Country:: Japan  
Priority Claimed:: Yes

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